

Nanotechnology for Nanoelectronics

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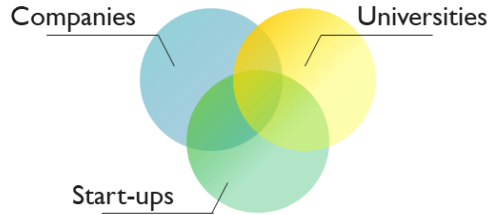
Materials & Components Analysis, IMEC

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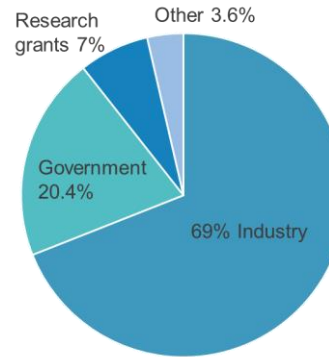


IMEC

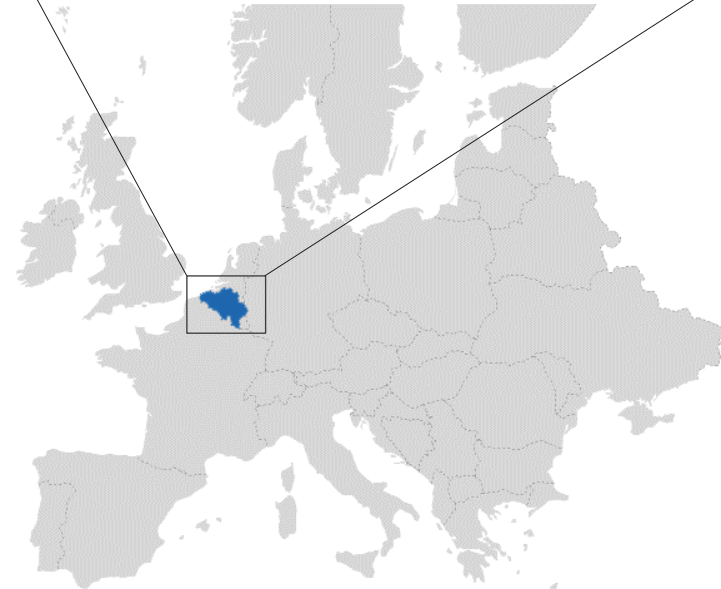
- R&D hub in nanoelectronics
- Founded in 1984
- 3500 employees
- Cleanroom 12000m²
- Focus on sub-10 nm CMOS



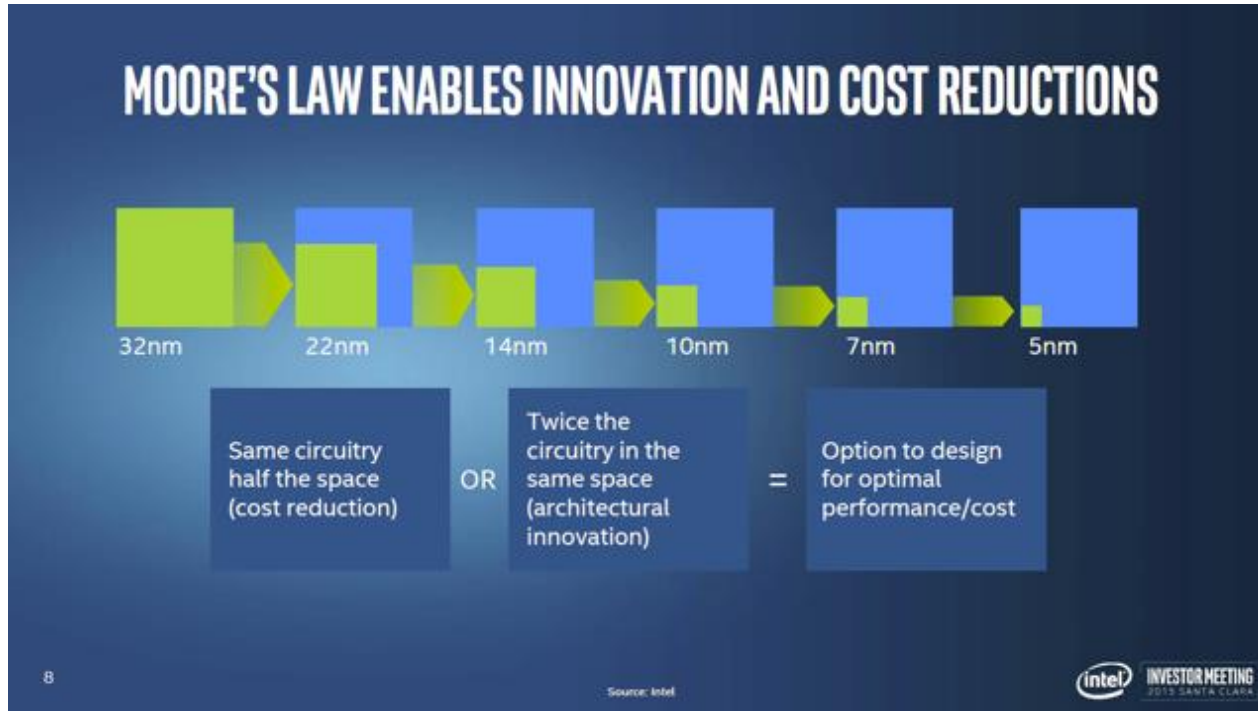
REVENUE 2019 > 500M€



OSAKA UNIVERSITY



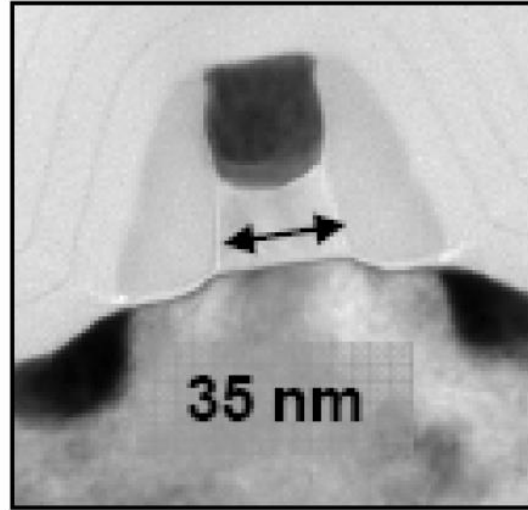
THE POWER OF SCALING



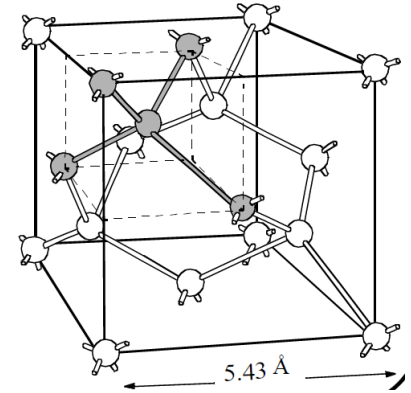
EVOLUTION OF TRANSISTOR DEVICES



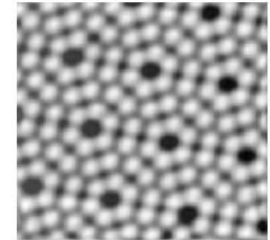
Yesterday's Transistor (1947)



Today's Transistor (2006)



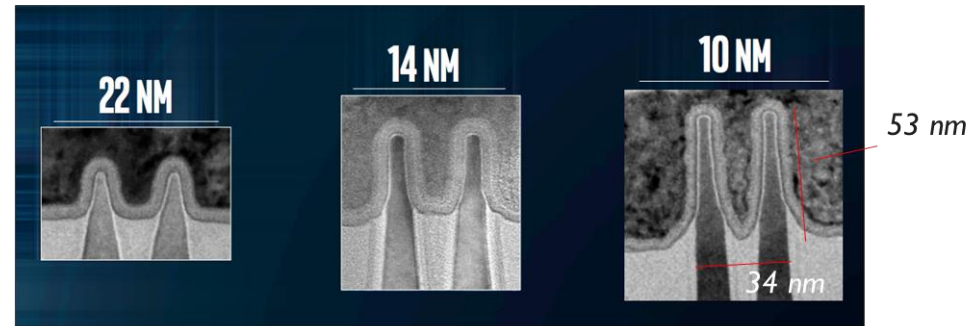
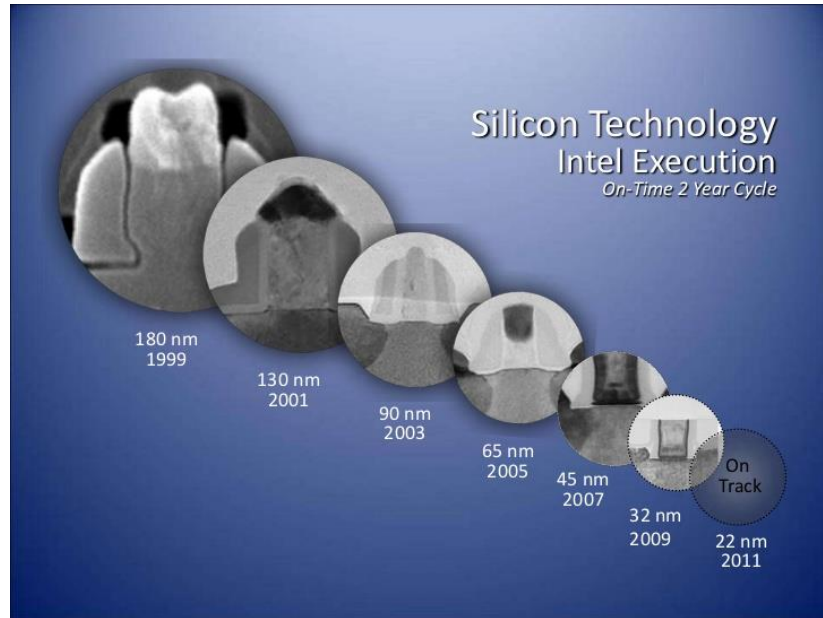
The magic of Si with its cubic lattice and 4 nearest neighbors



Si (111) plane

THE QUEST OF SCALING THE BULK-Si MOSFET

Twenty years of MOSFET innovation leading to FinFETs introduction and exiting future technologies



Source Intel

2.7x transistors density improvement

An aerial photograph of a city grid, likely New York City, showing a dense pattern of streets and buildings. The image is overlaid with a semi-transparent blue rounded rectangle at the bottom, which contains white text. The text box has a slight gradient and a soft shadow, blending into the background image.

Every second of the year, on average, over 8 trillion transistors are produced

IF TRANSISTORS WERE CARS..

The evolution in technology has enabled the electronic industry to continually produce devices and system that are smaller, more power efficient, richer features and lower in prices.

- If Automotive industry had achieved similar improvements in the last 30 years, a **Rolls-Royce**:
 - Would **cost \$ 40**
 - Could circle the globe **2 times** on **one liter gasoline**
 - Could have a top speed of **2.4 million miles/h**

[McKinsey]



THE ECONOMICS OF SCALING

TSMC Nanjing fab ~ 15-20 Billion USD investment for high volume manufacturing of 7nm and 5nm nodes

